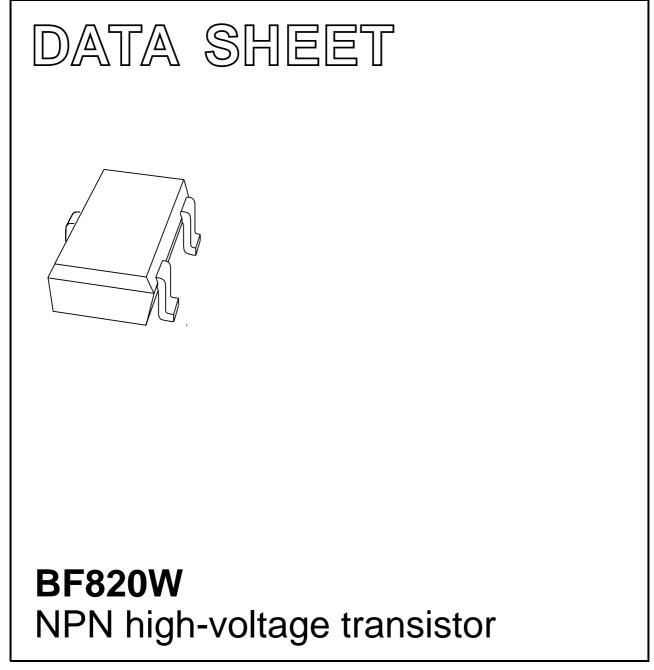
# DISCRETE SEMICONDUCTORS



Product data sheet Supersedes data of 1997 Sep 03 2003 Sep 09



### Product data sheet

## NPN high-voltage transistor

#### FEATURES

- Low current (max. 50 mA)
- High voltage (max. 300 V).

### APPLICATIONS

• Telephony and professional communication equipment.

#### DESCRIPTION

NPN high-voltage transistor in a SOT323 plastic package.

#### MARKING

TYPE NUMBER	MARKING CODE <sup>(1)</sup>		
BF820W	1V*		

#### Notes

- 1. \* = p : made in Hong Kong.
  - \* = t : made in Malaysia.
  - \* = W : made in China.

#### QUICK REFERENCE DATA

#### PINNING

PIN	DESCRIPTION	
1	base	
2	emitter	
3	collector	

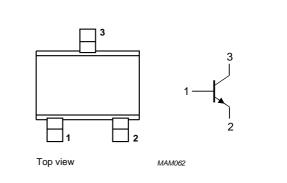


Fig.1 Simplified outline (SOT323) and symbol.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	-	300	V
V <sub>CEO</sub>	collector-emitter voltage	open base	-	300	V
I <sub>CM</sub>	peak collector current		-	100	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	-	200	mW
h <sub>FE</sub>	DC current gain	$I_{C} = 25 \text{ mA}; V_{CE} = 20 \text{ V}$	50	-	
C <sub>re</sub>	feedback capacitance	$I_{C} = i_{c} = 0; V_{CB} = 30 V; f = 1 MHz$	-	1.6	pF
f <sub>T</sub>	transition frequency	I <sub>C</sub> = 10 mA; V <sub>CE</sub> = 10 V; f = 100 MHz	60	_	MHz

## **BF820W**

# NPN high-voltage transistor

## **BF820W**

#### LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 60134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V <sub>CBO</sub>	collector-base voltage	open emitter	_	300	V
V <sub>CEO</sub>	collector-emitter voltage	open base	_	300	V
V <sub>EBO</sub>	emitter-base voltage	open collector	-	5	V
I <sub>C</sub>	collector current (DC)		_	50	mA
I <sub>CM</sub>	peak collector current		_	100	mA
I <sub>BM</sub>	peak base current		_	50	mA
P <sub>tot</sub>	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$ ; note 1	_	200	mW
T <sub>stg</sub>	storage temperature		-65	+150	°C
Тj	junction temperature		-	150	°C
T <sub>amb</sub>	operating ambient temperature		-65	+150	°C

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
R <sub>th j-a</sub>	thermal resistance from junction to ambient	note 1	625	K/W

#### Note

1. Transistor mounted on an FR4 printed-circuit board.

#### CHARACTERISTICS

 $T_j$  = 25 °C unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
I <sub>CBO</sub>	collector cut-off current	I <sub>E</sub> = 0; V <sub>CB</sub> = 200 V	-	10	nA
		$I_E = 0; V_{CB} = 200 V; T_j = 150 °C$	-	10	μΑ
I <sub>EBO</sub>	emitter cut-off current	I <sub>C</sub> = 0; V <sub>EB</sub> = 5 V	-	50	nA
h <sub>FE</sub>	DC current gain	I <sub>C</sub> = 25 mA; V <sub>CE</sub> = 20 V	50	-	
V <sub>CEsat</sub>	collector-emitter saturation voltage	I <sub>C</sub> = 30 mA; I <sub>B</sub> = 5 mA; note 1	-	600	mV
C <sub>re</sub>	feedback capacitance	$I_{C} = i_{c} = 0; V_{CB} = 30 V; f = 1 MHz$	-	1.6	pF
f <sub>T</sub>	transition frequency	$I_{C} = 10 \text{ mA}; V_{CE} = 10 \text{ V}; \text{ f} = 100 \text{ MHz}$	60	-	MHz

#### Note

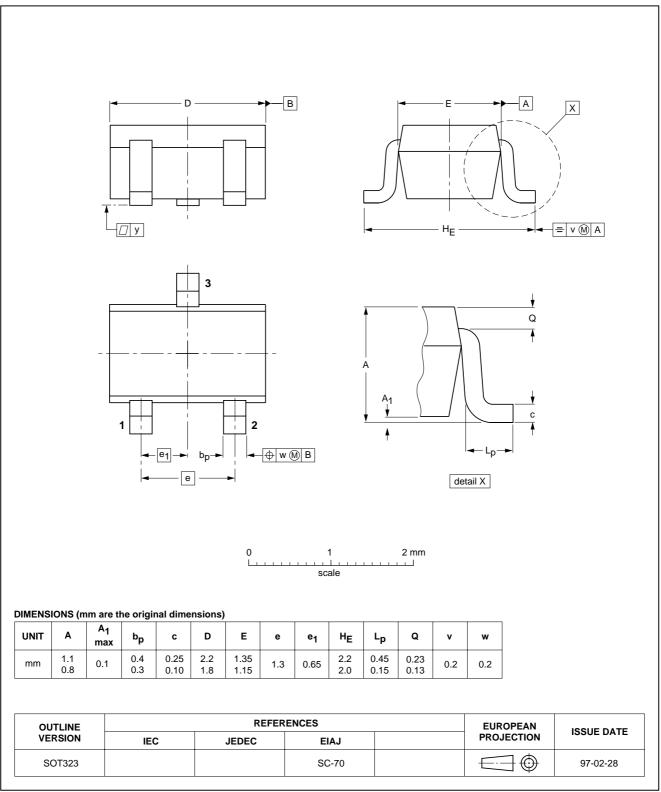
1. Pulse test:  $t_p \le 300 \ \mu s; \ \delta \le 0.02.$ 

**BF820W** 

# NPN high-voltage transistor

#### PACKAGE OUTLINE

#### Plastic surface mounted package; 3 leads



**SOT323** 

## NPN high-voltage transistor

**BF820W** 

#### DATA SHEET STATUS

DOCUMENT STATUS <sup>(1)</sup>	PRODUCT STATUS <sup>(2)</sup>	DEFINITION
Objective data sheet	Development	This document contains data from the objective specification for product development.
Preliminary data sheet	Qualification	This document contains data from the preliminary specification.
Product data sheet	Production	This document contains the product specification.

#### Notes

- 1. Please consult the most recently issued document before initiating or completing a design.
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# **NXP Semiconductors**

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#### **Contact information**

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